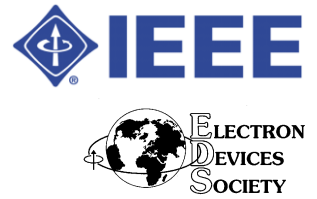


56th IEEE Semiconductor Interface Specialists Conference

December 10–13, 2025 (Tutorial: December 10)
Bahia Resort Hotel, San Diego, CA
www.ieeesisc.org



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Call for Papers

The SISC is a workshop-style conference that provides a forum for device engineers, solid state physicists, and material scientists to discuss topics of common interest, formally through invited and contributed presentations, and informally during poster and rump sessions. SISC is sponsored by the IEEE Electron Devices Society and is held right after IEDM.



This year, SISC will be held as a fully in-person event.

The program includes talks and poster presentations (*theory and experiment*) on the role of materials and their interfaces on performance and reliability of:

- **Logic Devices** for future technology nodes (Nanosheet, CFET, VFET, etc.),
- Insulators on **High-Mobility** substrates (SiGe, Ge, etc.),
- Non-Volatile Memory for AI / **In-Memory** / **Neuromorphic Compute** (ReRAM, PCM, ECRAM, etc.),
- **Wide Bandgap** semiconductor power devices (SiC, GaN, β -Ga₂O₃, etc.),
- **Ferroelectric** devices (FeFET, FeRAM, etc.),
- **Steep Sub-Threshold** slope logic devices (Tunnel FETs, etc.),
- **Low dimensional** materials and devices,
- **Monolithic and/or Heterogeneous** ICs (BEOL oxide transistors, interconnects, packaging, etc.),

including machine learning / materials discovery techniques developed and used for their study.

****Invited speakers and Wednesday evening Tutorial to be announced soon!****

A **Best Student Presentation Award** will be given in memory of E. H. Nicollian.

A **Best Poster Award** will be given in memory of T. P. Ma.



Abstract submission deadline: July 31, 2025

Abstract submission, previous technical programs, contact information, etc.: <https://www.ieeesisc.org>

(submission page will open in mid-June)